

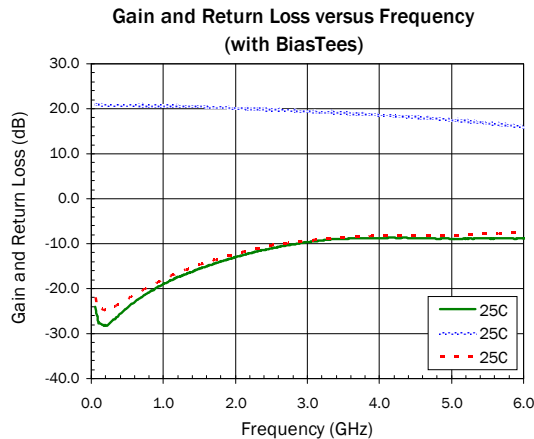


Product Description

RFMD's SBB5089Z is a high performance InGaP HBT MMIC amplifier utilizing a Darlington configuration with an active bias network. The active bias network provides stable current over temperature and process Beta variations. Designed to run directly from a 5V supply, the SBB5089Z does not require a dropping resistor as compared to typical Darlington amplifiers. The SBB5089Z product is designed for high linearity 5V gain block applications that require small size and minimal external components. It is internally matched to 50Ω.

Optimum Technology Matching® Applied

- GaAs HBT
- GaAs MESFET
- InGaP HBT
- SiGe BiCMOS
- Si BiCMOS
- SiGe HBT
- GaAs pHEMT
- Si CMOS
- Si BJT
- GaN HEMT
- RF MEMS



Features

- Wideband Flat Gain to 4GHz: +/-1.1dB
- $P_{1dB} = 20.4$ dBm at 1950MHz
- Single Fixed 5V Supply
- Robust 1000V ESD, Class 1C
- Patented Thermal Design and Bias Circuit
- Low Thermal Resistance

Applications

- PA Driver Amplifier
- Cellular, PCS, GSM, UMTS
- Wideband Instrumentation
- Wireless Data, Satellite Terminals

Parameter	Specification			Unit	Condition
	Min.	Typ.	Max.		
Small Signal Gain	19.0	20.5	22.0	dB	850MHz
	18.5	20.0	21.5	dB	1950MHz
	14.5	16.0	17.5	dB	6000MHz
Output Power at 1dB Compression		20.5		dBm	850MHz
	19.0	20.5		dBm	1950MHz
Third Order Intercept Point		38.5		dBm	850MHz
	33.0	35.0		dBm	1950MHz
Bandwidth		3000		MHz	Min. 10dB return loss (typ.)
Input Return Loss	10.0	14.0		dB	1950MHz
Output Return Loss	10.0	14.0		dB	1950MHz
Reverse Isolation		23.3		dB	1950MHz
Noise Figure		4.2	4.9	dB	1950MHz
Device Operating Voltage		5.0	5.25	V	
Device Operating Current	65.0	75.0	92.0	mA	
Thermal Resistance		69.9		°C/W	junction - lead

Test Conditions: $V_D = 5V$, $I_D = 75$ mA Typ., OIP_3 Tone Spacing = 1MHz, P_{OUT} per tone = -dBm, $T_L = 25$ °C, $Z_S = Z_L = 50\Omega$, Tested with Bias Tees

Absolute Maximum Ratings

Parameter	Rating	Unit
Device Current (I_D)	100	mA
Max Device Voltage (V_D)	5.5	V
Max RF Input Power	24	dBm
Max Operating Dissipated Power	0.55	W
Junction Temp (T_J)	+150	°C
Operating Temp Range (T_L)	-40 to +85	°C
Storage Temp Range	-40 to +150	°C
ESD Rating - Human Body Model (HBM)	Class 1C	
Moisture Sensitivity Level	MSL2	



Caution! ESD sensitive device.

Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to the device may reduce device reliability. Specified typical performance or functional operation of the device under Absolute Maximum Rating conditions is not implied.

RoHS status based on EUDirective2002/95/EC (at time of this document revision).

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Operation of this device beyond any one of these limits may cause permanent damage. For reliable continuous operation, the device voltage and current must not exceed the maximum operating values specified in the table on page one.

Bias Conditions should also satisfy the following expression:
 $I_D V_D < (T_J - T_L) / R_{TH}$, $J-1$ and $T_L = T_{LEAD}$

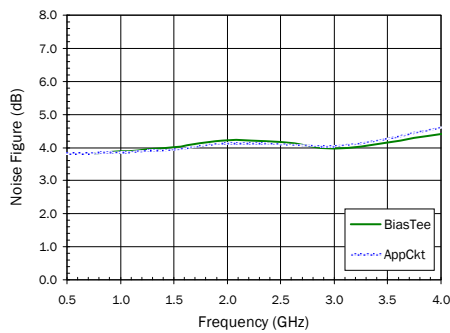
Typical Performance at Key Operating Frequencies (0.5GHz to 3.5GHz Application Circuit)

Parameter	Unit	500MHz	850MHz	1950MHz	2500MHz	3500MHz	4000MHz
Small Signal Gain	dB	20.8	20.8	20.1	19.8	18.7	17.8
Output Third Order Intercept Point	dBm	38.6	39.2	34.9	32.8	29.4	26.8
Output Power at 1dB Compression	dBm	20.5	20.4	20.4	19.4	16.9	14.7
Input Return Loss	dB	27.2	22.7	14.6	12.9	10.6	11.6
Output Return Loss	dB	31.8	21.5	13.5	12.0	13.5	27.5
Reverse Isolation	dB	22.7	22.8	23.4	23.7	24.7	25.7
Noise Figure	dB	3.8	3.8	4.1	4.1	4.3	4.6

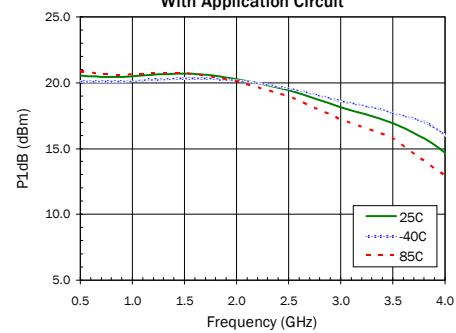
Test Conditions: $V_{CC} = 5V$, $I_D = 75mA$ Typ., OIP_3 Tone Spacing = 1MHz, P_{OUT} per tone = 0dBm, $T_L = 25^\circ C$, $Z_S = Z_L = 50\Omega$

Data on charts taken with 240MHz Application Circuit

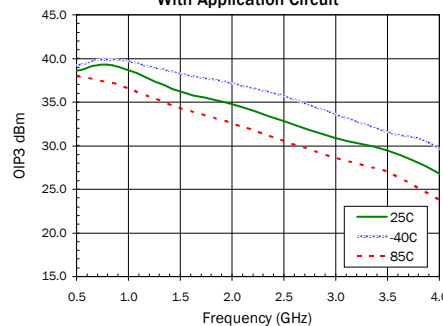
Noise Figure at 25°C



P1dB versus Frequency With Application Circuit

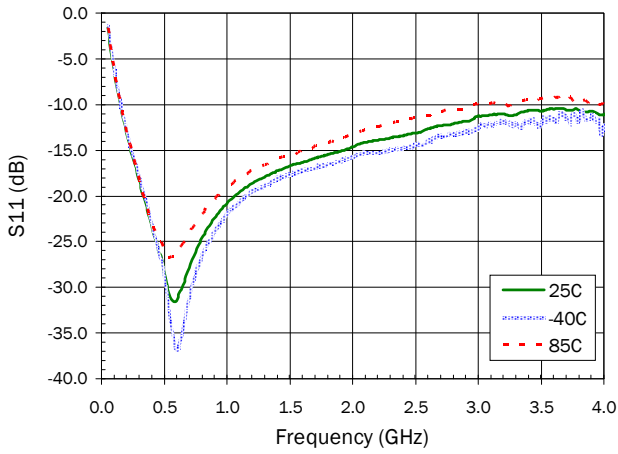


OIP3 versus Frequency With Application Circuit

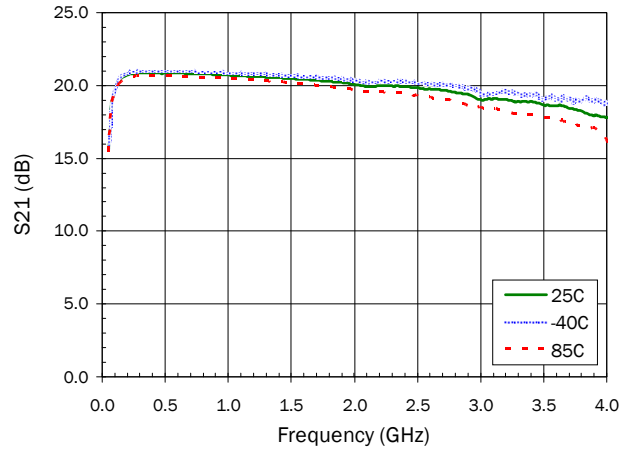


0.5GHz to 3.5GHz Application Circuit S-Parameters Over Temperature

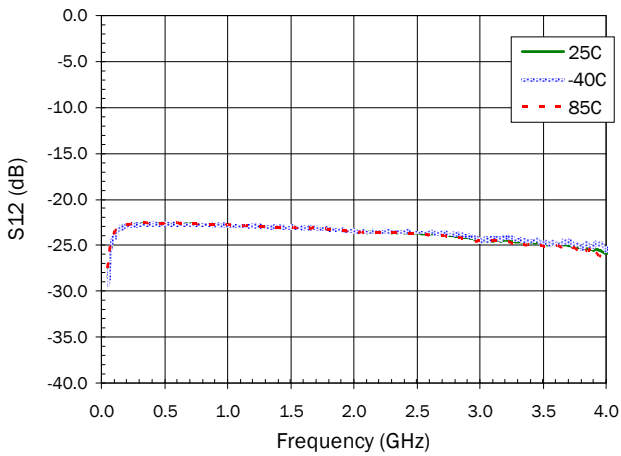
S11 versus Frequency



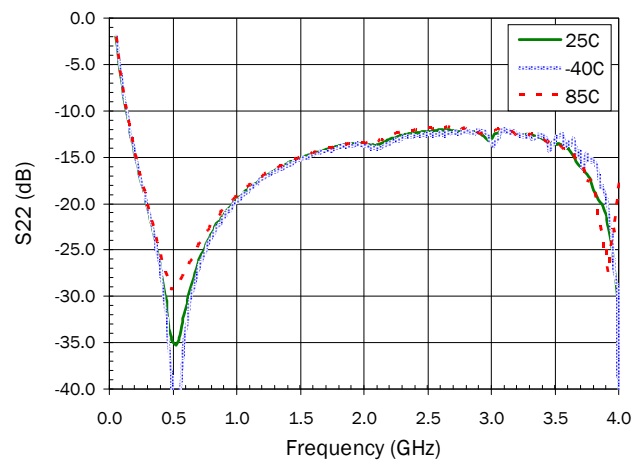
S21 versus Frequency



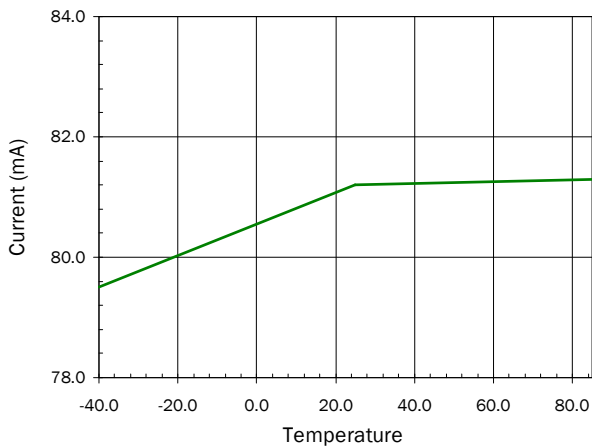
S12 versus Frequency



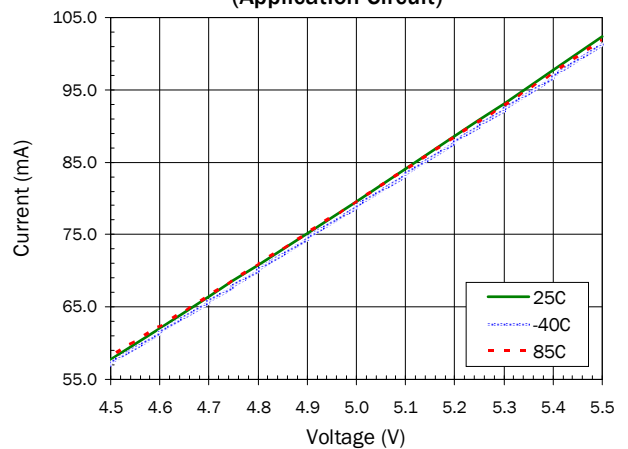
S22 versus Frequency



Id vs. Temperature (App. Ckt.)

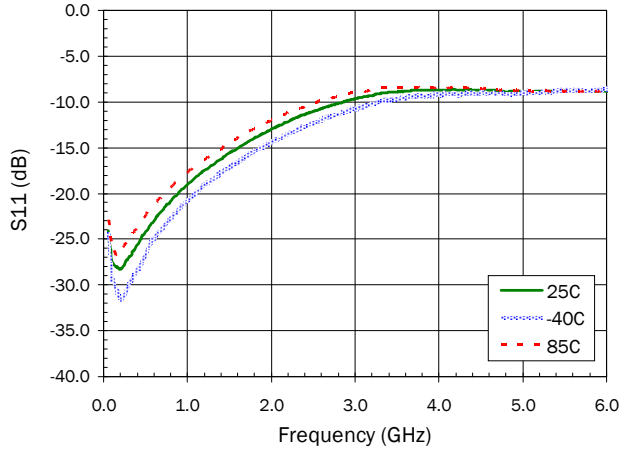


Current versus Voltage Over Temperature (Application Circuit)

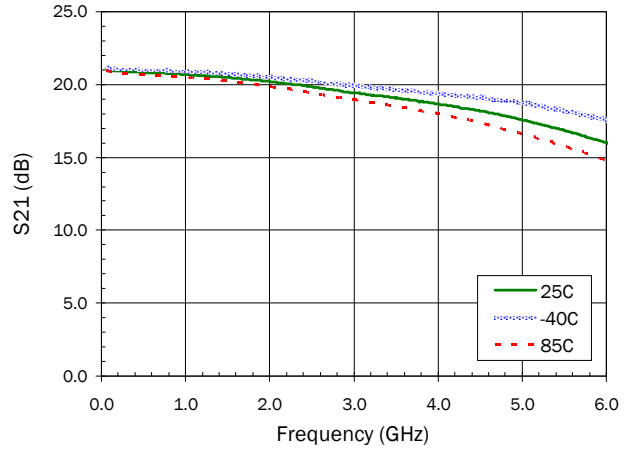


S-Parameters Over Temperature (Bias Tee)

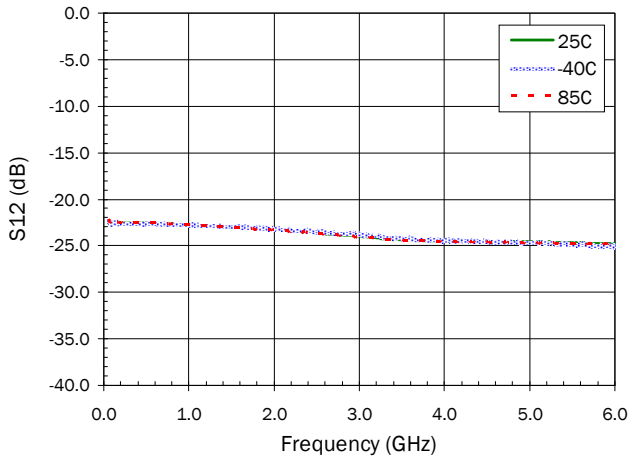
S11 versus Frequency



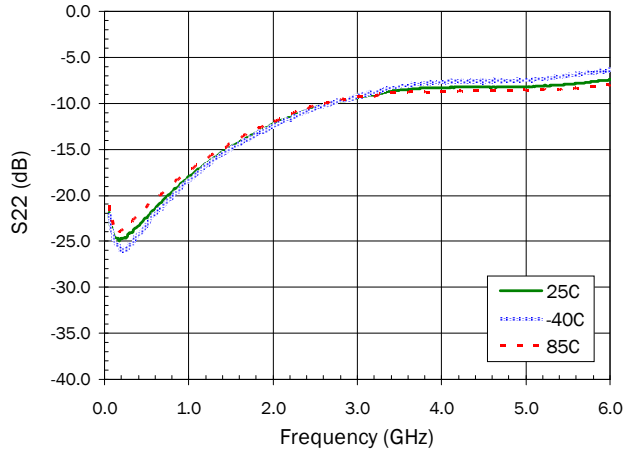
S21 versus Frequency



S12 versus Frequency

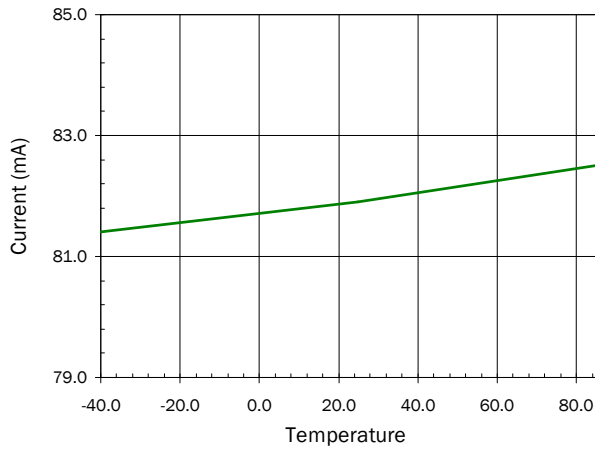


S22 versus Frequency

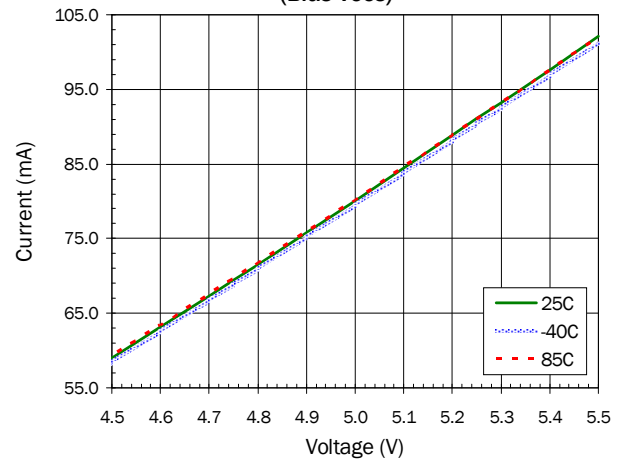


Device Current Over Temperature (Bias Tee)

Id vs. Temperature (Bias Tees)

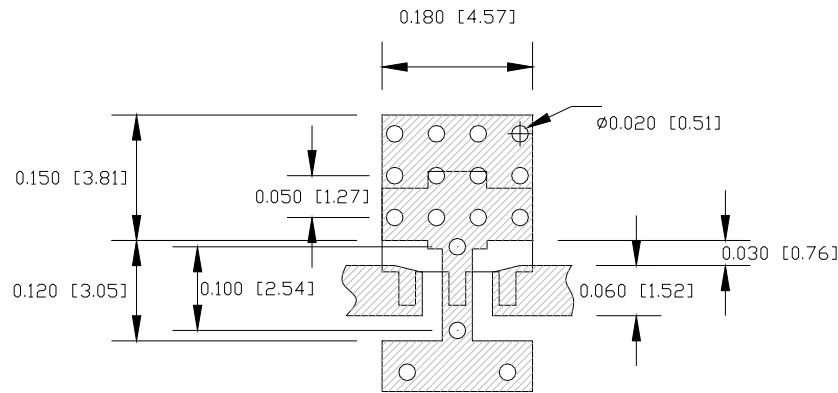


Current versus Voltage Over Temperature (Bias Tees)



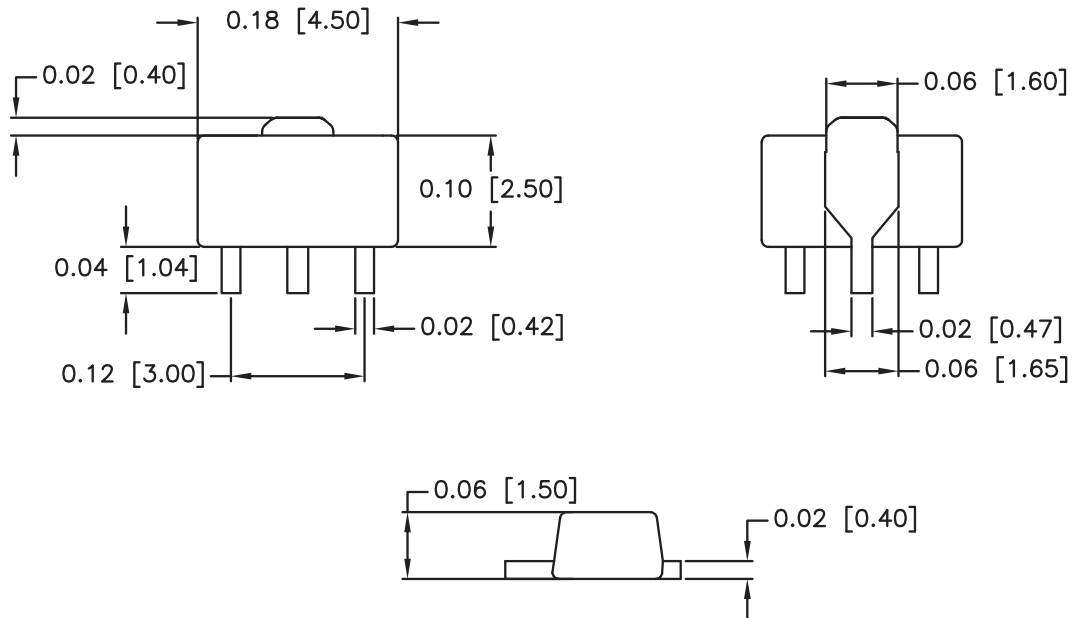
Pin	Function	Description
1	RF IN	RF input pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation.
2, 4	GND	Connection to ground. Use via holes for best performance to reduce lead inductance as close to ground leads as possible
3	RF OUT/ BIAS	RF output and bias pin. DC voltage is present on this pin, therefore a DC blocking capacitor is necessary for proper operation.

Suggested PCB Pad Layout

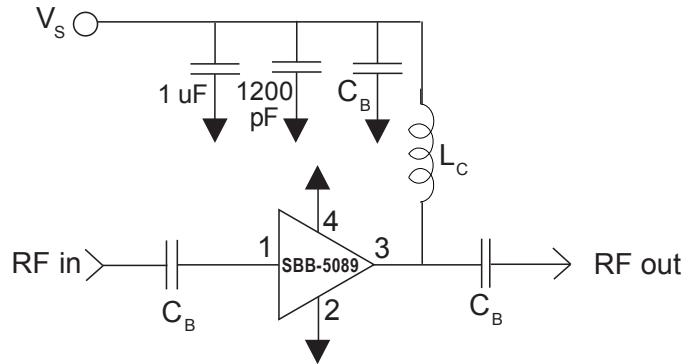


Nominal Package Dimensions

Dimensions in inches (millimeters)

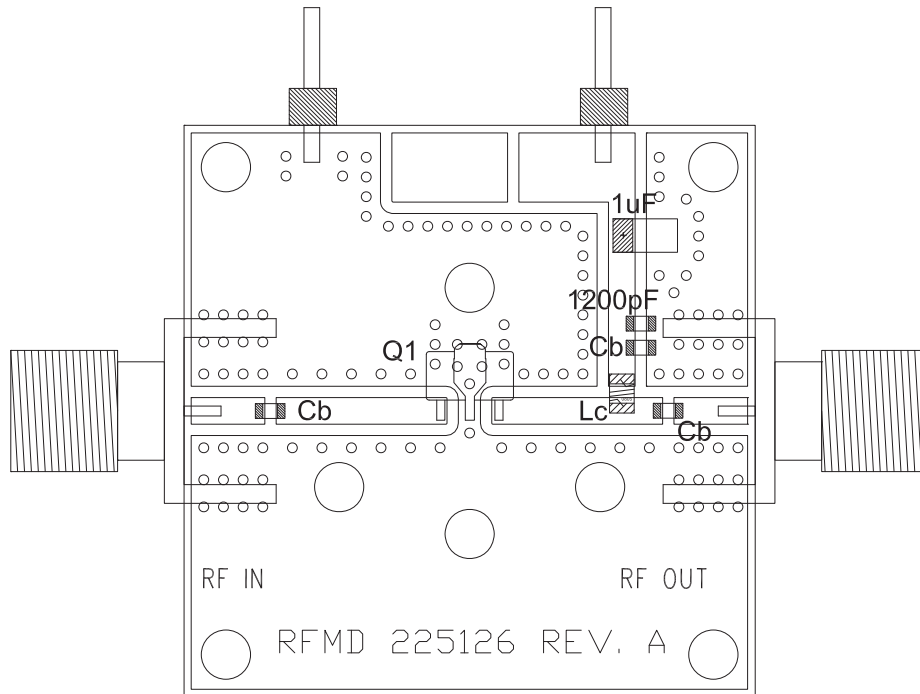


Application Schematic

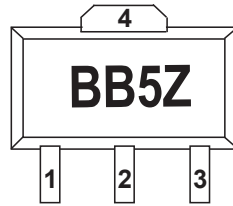


Reference Designator	Frequency (MHz) 500 to 3500
C _B	68pF
L _C	82nH 0805CS

Evaluation Board Layout



Part Identification



Alternate marking "SBB5089Z" on line 1 with Trace Code on line 2.

Ordering Information

Ordering Code	Description
SBB5089Z	7" Reel with 1000 pieces
SBB5089ZSQ	Sample Bag with 25 pieces
SBB5089ZSR	7" Reel with 100 pieces
SBB5089ZPCK	500MHz to 3500MHz PCBA with 5-piece sample bag